



**JSS Mahavidyapeetha  
JSS UNIVERSITY, NOIDA**

(Established under the Uttar Pradesh Private University Act No.12 of 2019)  
C-20/1, Sector-62 , Noida. GB Nagar -201301

**With The Devine Blessing of His Holiness  
Jagadguru Sri Shivarathri Deshikendra Mahaswamiji**

**Department of Electronics & Communication Engineering**

**CORDIALLY**

***INVITES***

**YOU TO**

**ONE DAY CONCLAVE ON**

***Semiconductors & Beyond: Building  
Powerful Computing Power***

**Date: 9<sup>th</sup> April, 2025**

**Time: 9:30 AM to 5:30 PM**

**Chief Guest**

**Mr. Sagar Sharma**

Head, Business Intelligence & Strategic Affairs  
@ Tata Electronics, Mumbai, India

**Guest of Honor**

**Mr. Sumant Parimal**

Founder & Director, Innogress Ventures,  
Noida, India

**Presided By**

**Dr. H R Mahadevaswamy**

Vice Chancellor (Officiating)  
JSS University, Noida

**Dr. Mamatha. T G**

Registrar  
JSS University, Noida

**Dr. R S Jagdish**

Organizing Chair  
Dean S/W and Academic Coordinator  
JSS University, Noida

**Dr. Gayatri Sakya**

Convener  
Associate Professor & HoD ECE,  
Assistant Registrar Research  
JSS University, Noida

**Organizing Committee:**

**Dr. Anuranjan Kansal (Assoc. Professor), Dr. Chandra Shankar (Assoc. Professor)  
Dr. Monika Malik (Asst. Professor), Mr. Ganesh HS (Asst. Professor)  
Dr. Pooja Pandey (Asst. Professor), Dr. Jyothishree Pillai (Asst. Professor)**

***All are Welcome***

# SCHEDULE

Time	Session
9:30 AM – 10:30 AM	Networking, Welcome and Inauguration Keynote Speech: “Building Semiconductor Ecosystem in India” by Mr. Sagar Sharma
10:30 AM – 11:15 AM	“Hardware security in the midst of AI frenzy” by Dr. Basant Kumar Dwivedi
11:15 AM – 11:30 AM	Tea Break
11:30 AM – 12:15 PM	“Tapping the Global semiconducting Sensor Market: Opportunities and Challenges” by Dr. Ravindra Jha
12:15 PM – 1:00 PM	“Machine learning for Semiconductor Industries” by Dr. Chandan Karfa
1:00 PM - 2:00 PM	Lunch Break
2:00 PM - 3:00 PM	“The Future of Computing from: From chips to AI driven Supercomputers” by Professor Satheesh Krishnamurthy
3:00 PM - 4:00 PM	“The Future of Quantum & Photonic Computing” . Quantum processors vs. classical supercomputers . Photonic computing for ultra-fast data processing . Commercial viability of quantum computing in the next decade by Mr. Sumant Parimal
4:00 PM - 4:15 PM	Tea Break
4:15 PM - 5:15 PM	Panel Discussion on Fireside Chat: "What’s Beyond Semiconductors?" Industry & Academic leaders discuss: Emerging trends: Molecular electronics, 2D materials (Graphene), Spintronics Future of AI-driven chip design and fully automated semiconductor R&D  Moderator: Mr. Sumant Parimal Member: Professor Satheesh Krishnamurthy, Mr. Sagar Sharma, Dr. Ravindra Jha
5:15 PM – 5:30 PM	Valedictory & Closing Remarks